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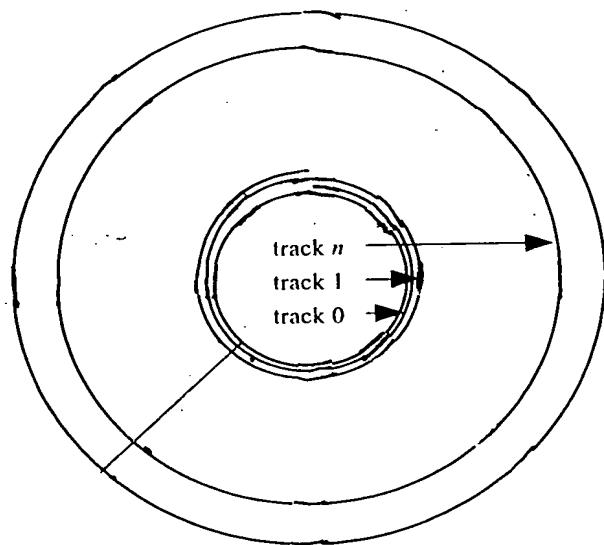


FIG. 1  
(Prior Art)

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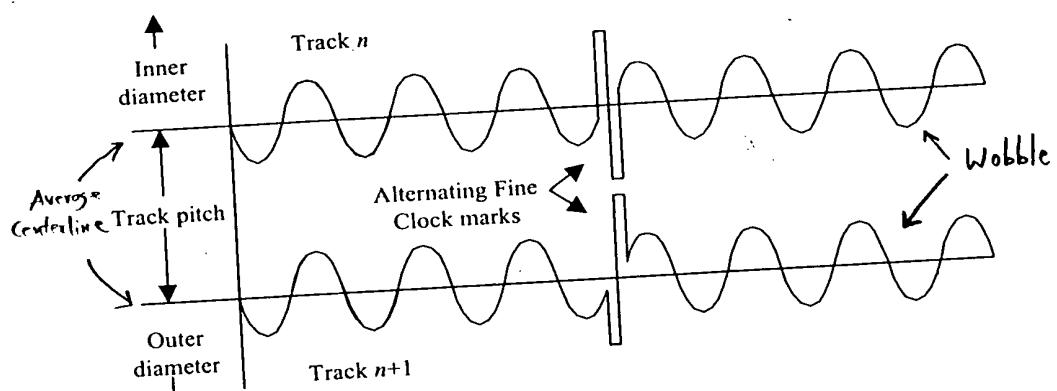
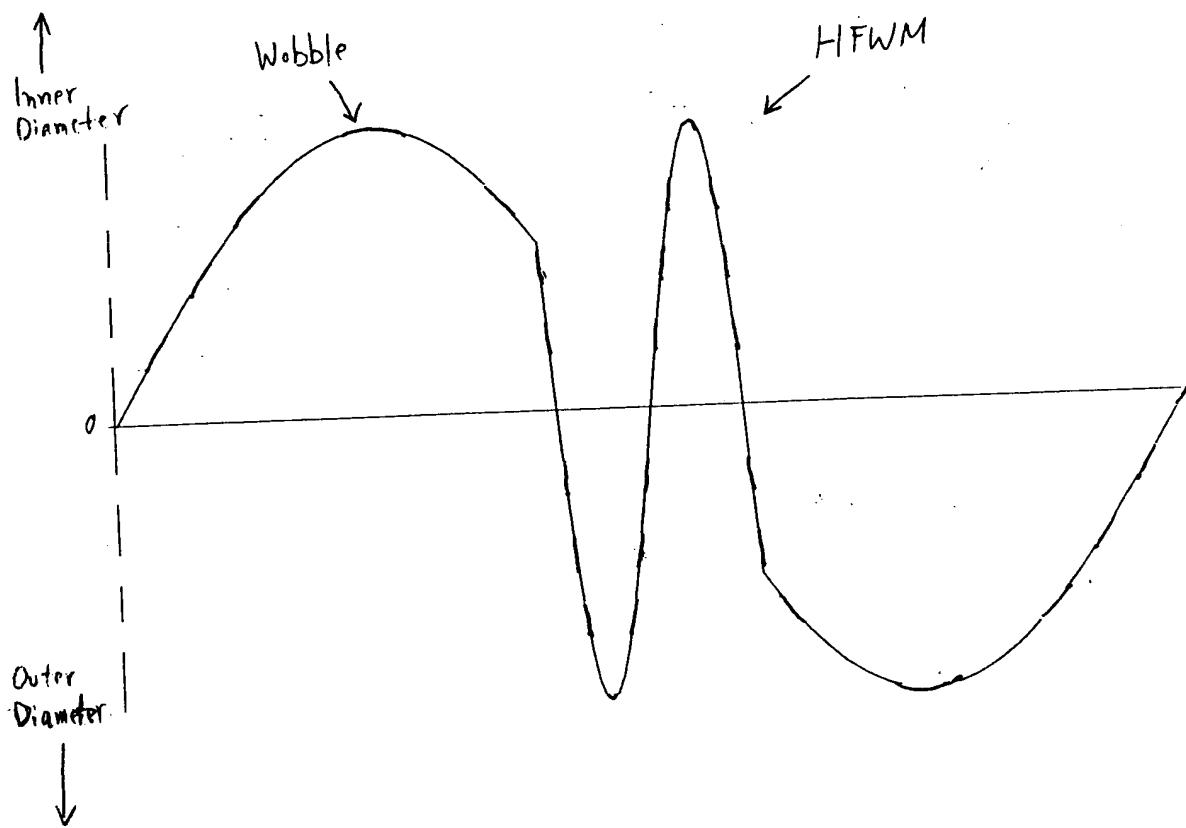


FIG. 2  
(Prior Art)



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FIG. 3

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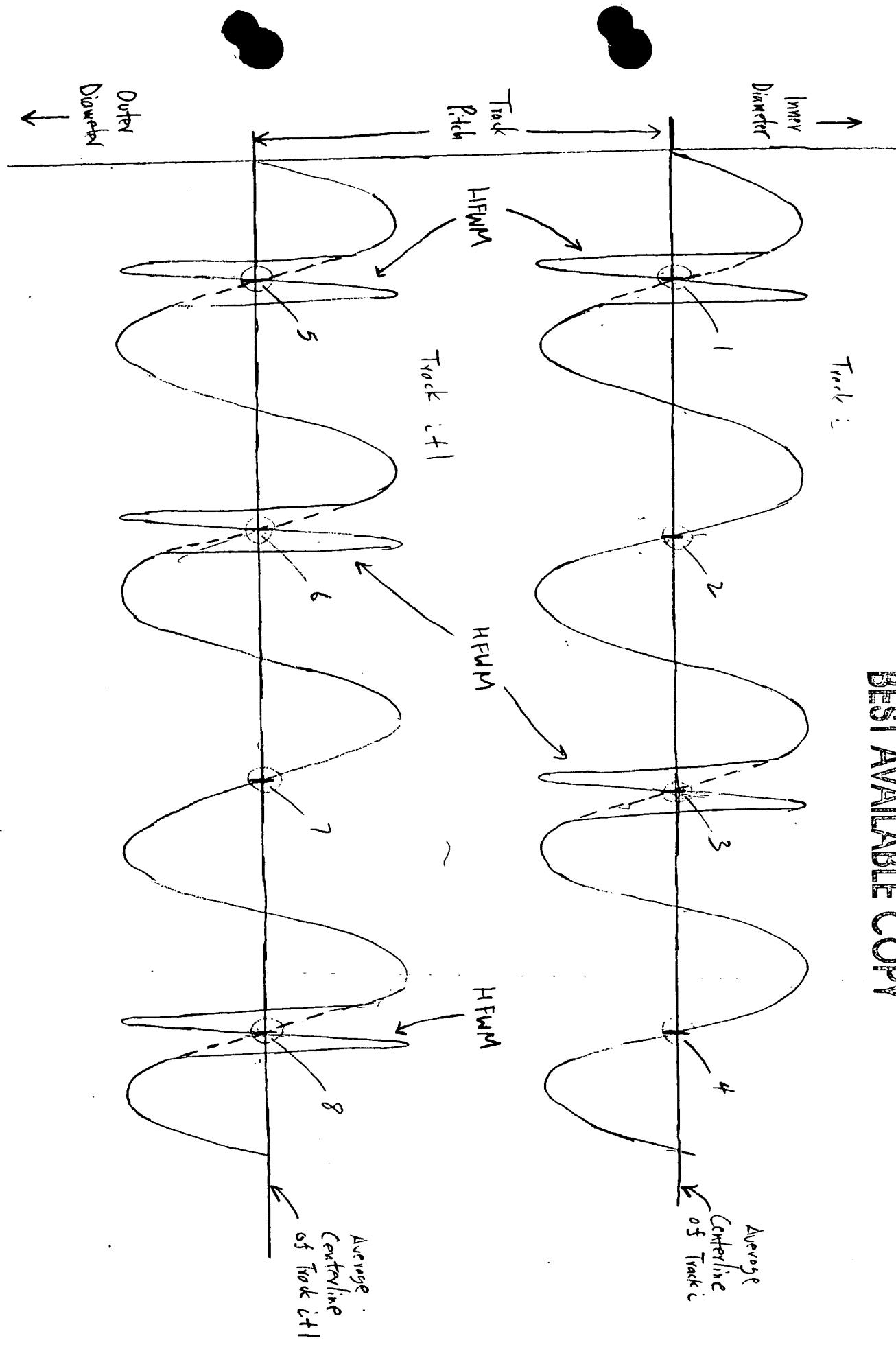


FIG. 4

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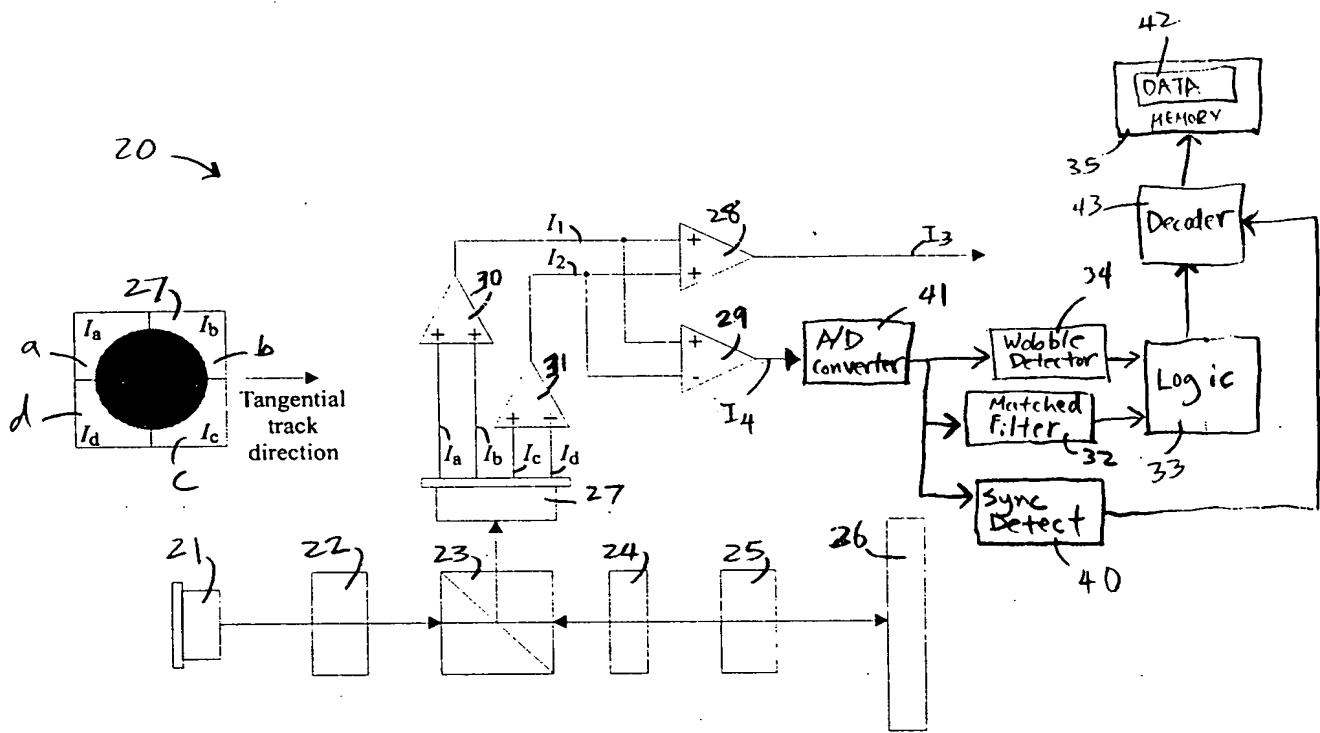


FIG. 5

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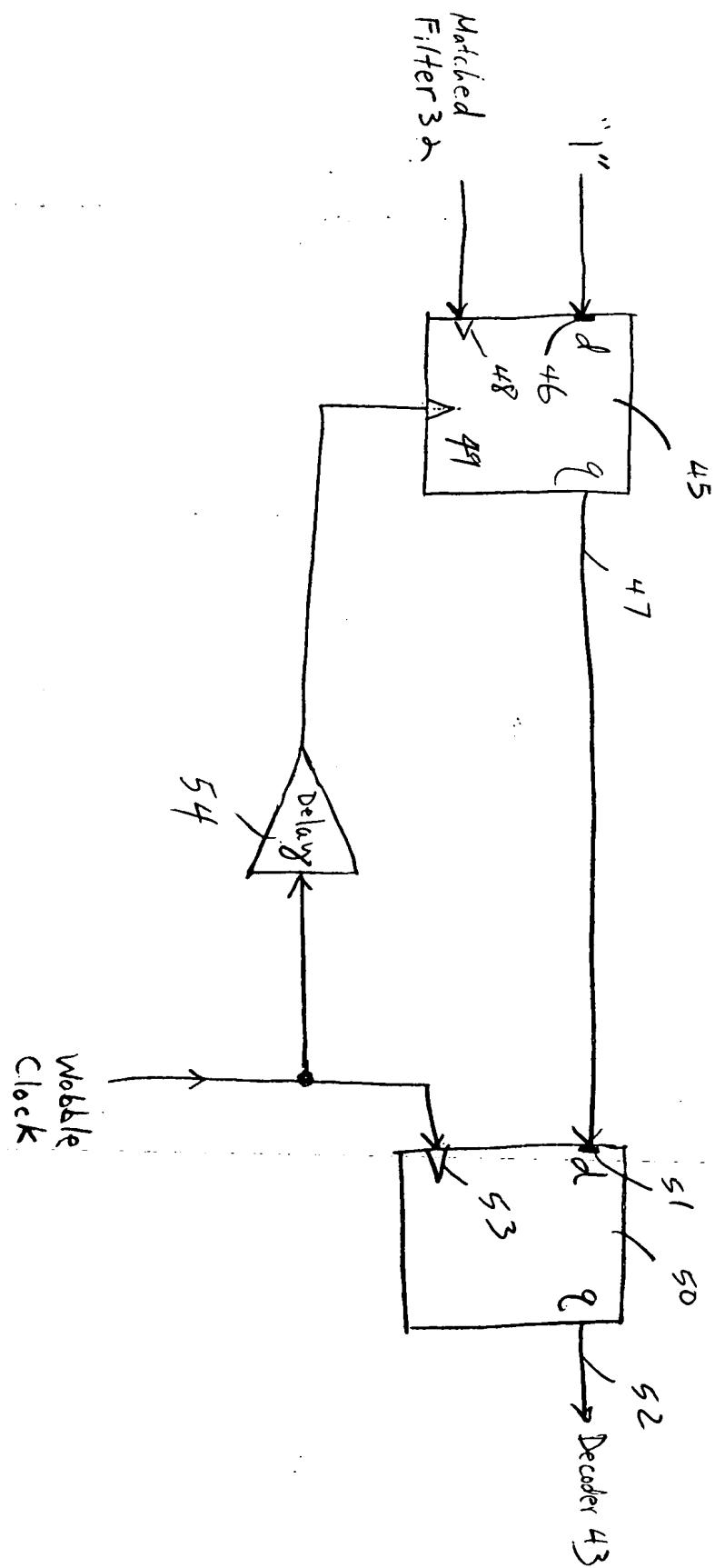
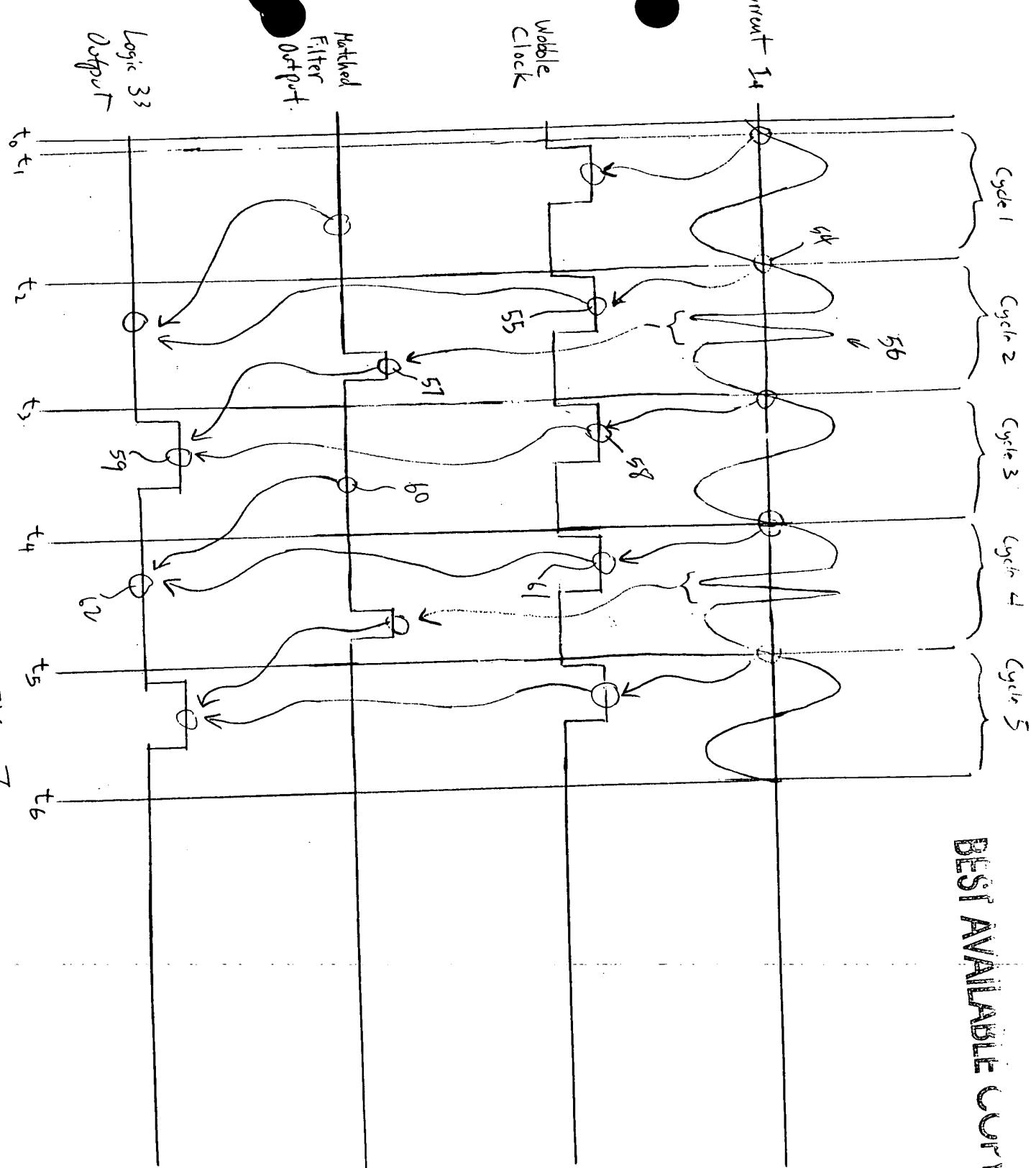


FIGURE 6  
LOGIC

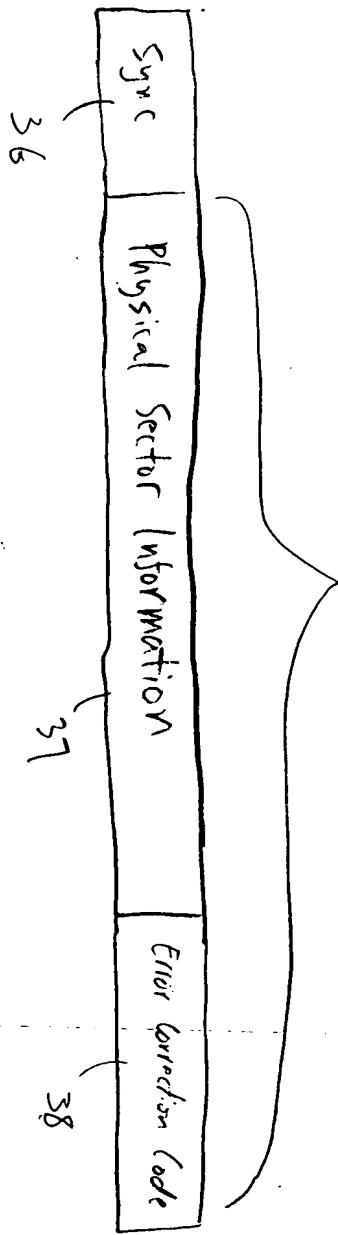


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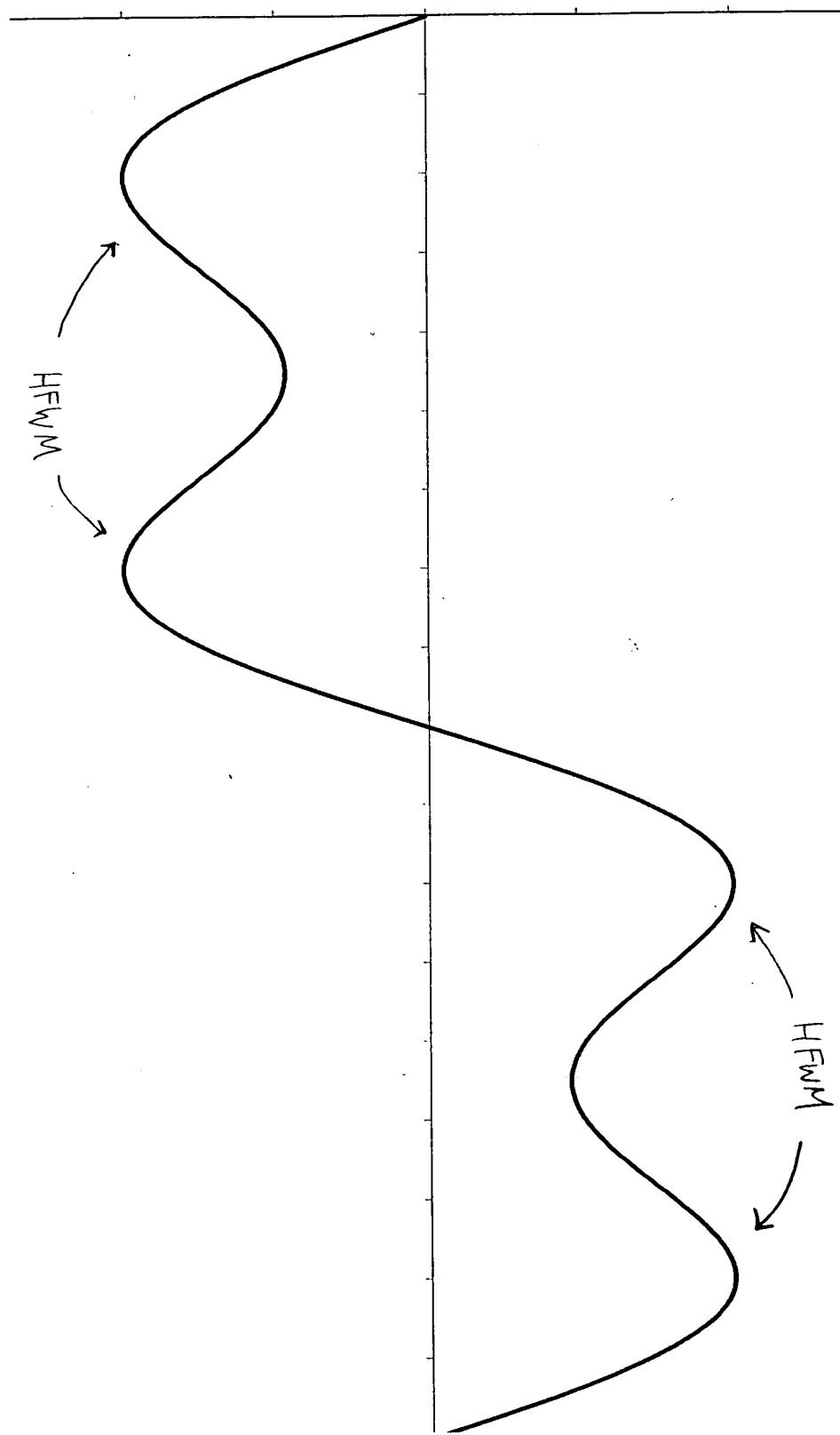


FIG. 9